

Name: Answer

Instructions: Do NOT use your book, notes or calculator and answer questions on test page (bottom and back if necessary).

1. Add the following signed 2's complement 8-bit word length numbers and specify if an overflow condition occurred. Show all carry bits. Write all answers in binary and hexadecimal. (Note parts a – d are binary numbers and part e are hexadecimal.)

Carry in and carry out shown in Red.

a) **0 1 1 1 1 1 1 0** b) **1 1 1 1 1 1 1 0** c) **0 0 0 0 1 0 0 0** d) **0 1 0 0 0 0 0 0** e)
 1 0 1 1 0 1 1 1 0 1 0 1 1 1 0 1 1 0 1 1 0 1 1 0 1 0 1 1 1 0 0 1 B9
 +1 0 1 0 1 1 0 1 +0 0 1 1 1 0 1 1 +0 1 0 0 0 1 0 1 +0 0 1 0 0 1 1 0 + 37
 [1]0 1 1 0 0 1 0 0 **[0]**1 0 0 1 1 0 0 0 **[0]**1 1 1 1 1 0 1 1 **[0]**1 1 0 1 1 1 1 1 F0₁₆
 64₁₆ - overflow 98₁₆ - overflow FB₁₆ - No ovflw DF₁₆ - No ovflw No ovflw

e)
 0 1 1 1 1 1 1 0
 1 0 1 1 1 0 0 1
 0 0 1 1 0 1 1 1
 [0]1 1 1 1 0 0 0 0

2. Using the procedure described in class, subtract the following signed 2's complement 4-bit word length numbers and specify if an overflow condition occurred. Show all carry bits. Write all answers in binary and hexadecimal. (Note parts a – d are binary numbers and part e are hexadecimal.)

a)	b)	c)	d)	e)
1 0 1 0	1 0 1 0	1 1 1 0	0 1 1 1	D
-1 1 0 1	-0 1 1 0	-0 0 1 1	-0 0 1 0	-4
0 1 0 1	0 1 1 1	1 0 0 1	1 1 1 1	1 1 1 1
1 0 1 0	1 0 1 0	1 1 1 0	0 1 1 1	1 1 0 1
<u>0 0 1 0</u>	<u>1 0 0 1</u>	<u>1 1 0 0</u>	<u>1 1 0 1</u>	<u>1 0 1 1</u>
[0] 1 1 0 1	[1] 0 1 0 0	[1] 1 0 1 1	[1] 0 1 0 1	[1] 1 0 0 1
D ₁₆ - no ovflw	4 ₁₆ - overflow	B ₁₆ - no ovflw	5 ₁₆ - no ovflw	9 ₁₆ - no ovflw